IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tu, et al.

Attorney Docket:

TSM03-0731

Filed:

Herewith

Examiner:

TBD

Serial No.:

TBD

Art Unit:

TBD

For:

Metal-Insulator-Metal Capacitors

Mail Stop: Patent Application Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A & 08B that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Pursuant to 37 CFR 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included copies of any non-patent literature.

Respectfully submitted,

3-26-04

Date

Roger C. Knapp Attorney for Applicant

Reg. No. 46,836

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PTO/SB/08a (08-03)
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U.S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ^{2 (# known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
····		US-5,598,367	01-28-1997	Noble			
·····		US-5,866,451	02-02-1999	Yoo et al.			
		US-6,141,242	10-31-2000	Hsu et al.			
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	FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶			

Examiner	Date	
Signature	Considered	

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Translation is attached.

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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²			
		LIU, R., et al., "Single Mask Metal-Insulator-Metal (MIM) Capacitor with Copper Damascene Metallization for Sub-0.18µm Mixed Mode Signal and System-on-a-Chip (SoC) Applications," IEEE (2000) pp. 111-113.				
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